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(12) United States Patent Cooper

(54) MOS-BASED POWER SEMICONDUCTOR DEVICE HAVING INCREASED CURRENT CARRYING AREA AND METHOD OF FABRICATING SAME

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(57) **ABSTRACT**

A semiconductor device includes a substrate, a drift region, a source region, a gate region, a drain contact and a base region. The substrate is doped with a first dopant type. The drift region is disposed above the substrate, and is doped with the first dopant type. The source region, doped with the first dopant type. The gate region is disposed above the drift region and above the source region. The base region is disposed between the source region and the drift region. At least a portion of the base region includes at least one trench having a vertical wall and a horizontal wall. The base region is further configured to conduct current in a horizontal direction on the vertical wall and in a horizontal direction the horizontal wall.

20 Claims, 13 Drawing Sheets

